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#### **NEW QUESTION: 1**

Which of the following cache levels are implemented on microprocessors?

Each correct answer represents a complete solution. Choose two.

- A. Level 0 (L0) cache
- B. Level 1 (L1) cache
- C. Level 2 (L2) cache
- D. Level 5 (L5) cache

**Answer: B,C**

Explanation:

The Level 1 (L1) cache is implemented on microprocessors. The L1 cache is a type of memory implemented inside the microprocessor chip. It is the fastest memory in the computer. It contains the current working set of data and code. Cache memory is used to store frequently used information, so that the processor can access this information without delay. Level 2 (L2) cache is employed between main memory and L1 cache. The L2 cache contains additional data and code. In old architecture, L2 cache is mounted on the motherboard, which means that it runs at the motherboard's speed. In modern architecture, L2 caches are built directly into the microprocessor. Answer options C and A are incorrect. There are no such cache levels as level 0 and level

5. Reference: Contents: "[http://en.wikipedia.org/wiki/CPU\\_cache](http://en.wikipedia.org/wiki/CPU_cache)"

### **NEW QUESTION: 2**

Refer to the exhibit.

PC1 pings PC2. What three things will CORE router do with the data that is received from PC1? (Choose three.)

- A. CORE router will replace the destination IP address of the packets with the IP address of PC2.
- B. CORE router will replace the MAC address of PC2 in the destination MAC address of the frames.
- C. The data frames will be forwarded out interface FastEthernet0/1 of CORE router.
- D. CORE router will put the IP address of the forwarding FastEthernet interface in the place of the source IP address in the packets.
- E. CORE router will put the MAC address of the forwarding FastEthernet interface in the place of the source MAC address.
- F. The data frames will be forwarded out interface FastEthernet1/0 of CORE router.

**Answer: B,E,F**

### **NEW QUESTION: 3**

Identify three key capabilities of Oracle Bare Metal Cloud Service. (Choose three.)

- A. it provides High Availability (HA)
- B. it provides disk images in RAW format
- C. it provisions compute hardware that is managed by the customer
- D. it provides bare metal compute, high-performance storage,

and non-blocking networking

**E.** it enables non-uniform memory access (NUMA)

**Answer: A,C,D**

Explanation:

Explanation/Reference:

Reference:

<https://docs.cloud.oracle.com/iaas/Content/Compute/Concepts/computeoverview.htm>

#### **NEW QUESTION: 4**

HOTSPOT

#### **Answer :**

Explanation:

Box 1: Schema

Box 2: Domain Users

Explanation:

Box 1: Schema

The -d parameter sets the distinguished name of the search base for data export. Here the distinguished name is Schema.configuration.contoso.com  
Box 2: Domain user  
No special permissions are required to export the schema from the source forest, and any domain user can perform this task.

Reference: Export, Compare, and Synchronize Active Directory Schemas

<https://technet.microsoft.com/en-us/magazine/2009.04.schema.aspx>

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